

16-Bit 1.8-2.5V to 3.3V Level Shifting Transceiver with 3-State Outputs

Features

- PI74AVC164245 is designed for low voltage operation: 1.8V-2.5V to 3.3V
- Industrial operation at -40°C to +85°C
- Packaging (Pb-free & Green available):
 - -48-pin plastic 300-mil SSOP (V)
 - -48-pin plastic 240-mil TSSOP(A)
 - -48-pin plastic 173-mil TVSOP(K)

Truth Table (each 8-bit section)

In	puts	Oneration
ŌĒ	DIR	Operation
L	L	B data to A bus
L	Н	A data to B bus
Н	X	Isolation

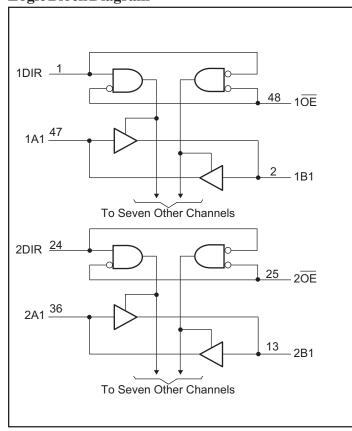
Description

Pericom Semiconductor's PI74AVC164245, a 16-bit (dual-octal) noninverting bus transceiver, contains two separate supply rails: B port (V_{CCB}), set at 3.3V, and A port (V_{CCA}), set to operate at 1.8-2.5V. This arrangement permits translation from a 1.8-2.5V to 3.3V environment and vice versa.

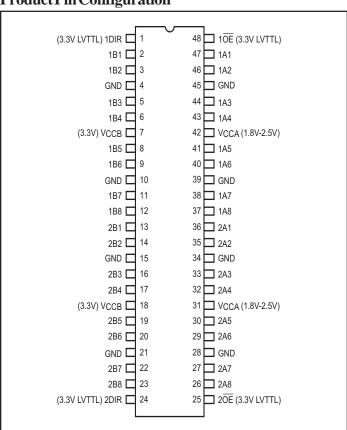
The PI74AVC164245 is designed for asynchronous communication between data buses.

To ensure the high impedance state during power up or power down, the output-enable (\overline{OE}) input should be tied to V_{CC} through a pullup resistor: the minimum value of the resistor is determined by the current-sinking capability of the driver.

Logic Block Diagram



Product Pin Configuration





Maximum Ratings

(Absolute maximum ratings over operating free-air temperature range from V_{CCB} at 3.3V & V_{CCA} at 1.5 - 2.5V, unless otherwise noted)

Stresses greater than those listed under MAXIMUM RATINGS may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect reliability.

Supply voltage range: V _{CCA} 0.5V to 3.8V	Continuous output current, IO±50mA
V _{CCB} 0.5V to 4.6V	Continuous current through each V _{CC} or GND±100mA
Input Voltage Range, V _I Except I/O ports ⁽¹⁾ 0.5V to 4.6V	Package thermal impedance, $0_{JA}^{(3)}$: A package 89°C/W
Input Voltage Range, V _I Except I/O ports ⁽¹⁾ 0.5V to 4.6V I/O port A ⁽²⁾ 0.5V to V _{CCA} +0.5V I/O port B ⁽¹⁾ 0.5V to V _{CCB} +0.5V	K package 58°C/W
Input clamp current, $I_{IK}(V_I < 0)$	V package 94°C/W
Output clamp current, I _{OK} (V _O <0) –50mA	Storage temperature range, T _{STG} 65°C to 150°C

$\textbf{Recommended Operating Conditions for } V_{CCB} \, at \, \textbf{3.3} V^{(4)}$

		Min.	Max.	Units
V_{CCB}	Supply voltage	3.0	3.6	
V_{IH}	High-level voltage	2		
V_{IL}	Low-level voltage		0.8	V
V_{IB}	Input Voltage	0	V_{CCB}	
V_{OB}	Output Voltage	0	V_{CCB}	
I_{OH}	High-level output current		-12	A
I_{OL}	Low-level output current		12	mA
$\Delta t/\Delta v$	Input transition rise or fall rate		10	ns/V
T _A	Operating free-air temperature	-40	85	°C

Recommended Operating Conditions for V_{CCA} at 2.5 $V^{(4)}$

			Min.	Max.	Units
V_{CCA}	Supply voltage		2.3	2.7	
V_{IH}	High-level voltage	$V_{CCA} = 2.3V \text{ to } 2.7V$	1.7		
V_{IL}	Low-level voltage	$V_{CCA} = 2.3V \text{ to } 2.7V$		0.7	V
V_{IA}	Input voltage		0	V _{CCA}	
V_{OA}	Output voltage	0	V _{CCA}		
T	High-level output current	$V_{CCA} = 2.3V$		-8	
I_{OH}	riigii-ievei output current	$V_{CCA} = 2.7V$		-12	mA
Τ	Low-level output current	$V_{CCA} = 2.3V$		8	
I_{OL}	Low-level output current	$V_{CCA} = 2.7V$		12	
$\Delta t/\Delta v$	Input transition rise or fall rate			10	ns/V
T _A	Operating free-air temperature		-40	85	°C

Notes:

- 1. This value is limited to 4.6V maximum.
- 2. This value is limited to 3.8V maximum.
- 3. The package thermal impedance is calculated in accordance with JESD 51.
- 4. To ensure proper device operation, all unused device inputs must be held at the associated V_{CC} or GND.



$\textbf{Recommended Operating Conditions for } V_{\textbf{CCA}} \, \textbf{at 1.8} V^{(1)}$

			Min.	Max.	Units
V _{CCA}	Supply voltage		1.65	1.95	
V_{IH}	High-level voltage	$V_{CCA} = 1.65V$ to 1.95V	0.65 x V _{CCA}		
V_{IL}	Low-level voltage	$V_{CCA} = 1.65V$ to 1.95V		0.35 x V _{CCA}	V
V_{IB}	Input voltage		0	V_{CCA}	
V _{OA}	V _{OA} Output voltage			V_{CCA}	
I	High lavel output ourrent	$V_{CCA} = 1.65V$		-4	
I_{OH}	High-level output current	$V_{CCA} = 1.95V$		-8	mA
Ī	Low-level output current	$V_{CCA} = 1.65V$		4	
I_{OL}	Low-level output current	$V_{CCA} = 1.95V$		8	
Δt/Δv	Input transition rise or fall rate			10	ns/V
T _A	Operating free-air temperature		-40	85	°C

Notes:

$\textbf{Electrical Characteristics} \ (\textbf{V}_{CCB}) \ (\textbf{Over recommended operating free-air temperature range for V}_{CCB} = 3.3 \text{V})$

P	arameters	Test Conditions	V _{CCB}	Min.	Typ. [†]	Max.	Units
		L	3.0V	2.8			
V (A (- D)	$I_{OH} = -100\mu A$	3.6V	3.4			
VOH (A	A to B)	124	3.0V	2.2			
		$I_{OH} = -12 \text{mA}$	3.6V	2.8			V
		1004	3.0V			0.2	V
17 (A) (a D)	$I_{OL} = 100 \mu A$	3.6V			0.2	
$V_{OL}(A)$	A 10 B)	L. 12mA	3.0V			0.5	
		$I_{OL} = 12 \text{mA}$	3.6V			0.5	
II	Control Inputs	V _I = V _{CCB} or GND	3.6V			±5	
I _{OZ} ‡	A or B ports	$V_{O} = V_{CCB}$ or GND	3.6V			±10	^
I_{CC}	$V_{I} = V_{CCB}$ or GND, $I_{O} = 0$		3.6V			20	μΑ
ΔI_{CC}^* One input at V_{CCB} –0.6V, Other inputs at V_{CCB} or GND		3.0V to 3.6V			500		
CI	Control Inputs	V _I = V _{CCB} or GND	3.3V		4.5		nΕ
C _{IO}	A or B ports	$V_{O} = V_{CCB}$ or GND	3.3V		6.5		pF

Notes:

^{1.} To ensure proper device operation, all unused device inputs must be held at the associated V_{CC} or GND.

[†] Typical values are measured at $V_{CC} = 3.3V$, $T_A = 25$ °C

[‡] For I/O ports, the parameter I_{OZ} includes the input leakage current.

^{*} This is the increase in supply current for each input that is at one of the specified TTL voltage levels rather than at 0 or the associated V_{CC}.



Electrical Characteristics (V_{CCA}) (Over recommended operating free-air temperature range for $V_{CCA} = 2.5 \text{V}$)

Pa	arameters	Test Conditions	V _{CCA}	Min.	Typ. [†]	Max.	Units
I _{OH}		$I_{OH} = -100\mu A$	2.3V to 2.7V	V _{CC} -0.1			
V (D	40. 4)	L O A	2.3V	1.7			
V _{OH} (B	10 A)	$I_{OH} = -8mA$	2.7V	2.0			
		$I_{OH} = -12mA$	2.7V	1.8			V
		$I_{OL} = 100\mu A$	2.3V to 2.7V			0.2	
V _{OL} (B	to A)	$I_{OL} = 8mA$	2.3V			0.4	
		$I_{OL} = 12mA$	2.7V			0.5	
II	Control Inputs	V _I = V _{CCA} or GND	2.7V			±5	
I _{OZ} ‡	A or B ports	V _O = V _{CCA} or GND	2.7V			±10	4
Icc	I_{CC} $V_{I} = V_{CCA}$ or GND, $I_{O} = 0$		2.7V			30	μΑ
ΔI_{CC}^*		One input at V_{CCA} –0.6V, Other inputs at V_{CCA} or GND	2.3V to 2.7V			500	
CI	Control Inputs	V _I = V _{CCA} or GND	2.5V		4.5		T
C _{IO}	A or B ports	V _O = V _{CCA} or GND	2.5V		6.5		pF

Notes:

- † Typical values are measured at $V_{CC} = 3.3V$, $T_A = 25$ °C
- ‡ For I/O ports, the parameter I_{OZ} includes the input leakage current.
- * This is the increase in supply current for each input that is at one of the specified TTL voltage levels rather than at 0 or the associated $V_{\rm CC}$.

Electrical Characteristics (V_{CCA}) (Over recommended operating free-air temperature range for $V_{CCA} = 1.8V$)

Pa	arameters	Test Conditions	V _{CCA}	Min.	Тур.**	Max.	Units
	$I_{OH} = -100\mu A$		1.65V to 1.95V	V _{CC} -0.1			
W (D	40. 4)	I	1.65V	1.3			
V _{OH} (B	10 A)	$I_{OH} = -4mA$	1.95V	1.4			
		$I_{OH} = -8mA$	1.95V	1.3			V
		$I_{OL} = 100\mu A$	1.65V			0.2	
V _{OL} (B	to A)	$I_{OL} = 4mA$	1.95V			0.3	
		$I_{OL} = 8mA$	1.95V			0.4	
Ioz***	A or B ports	$V_{O} = V_{CCA}$ or GND	1.95V			±5	
Icc		$V_I = V_{CCA}$ or GND, $I_O = 0$	1.95V			±20	μΑ
ΔI_{CC}^*		One input at $V_{\rm CCA}$ –0.6V, Other inputs at $V_{\rm CCA}$ or GND	1.65V to 1.95V			300	
CI	Control Inputs	V _I = V _{CCA} or GND	1.8V		4.5		E
C _{IO}	A or B ports	V _O = V _{CCA} or GND	1.8V		6.5		pF

Notes:

^{*} The increase in supply current for each input at one of the specified TTL voltage levels rather than at the associated V_{CC}.

^{**} Typical values are measured at $V_{CC} = 2.5V$ or 1.8V, $T_A = 25$ °C

^{***} For I/O ports, the parameter IOZ includes the input leakage current.



Switching Characteristics

(Over recommended operating free-air temperature range, C_L = 30pF (unless otherwise noted), see Figures 1, 2, 3

			PI74AV		C 164245		PI74AVC164245A			\	
				$V_{CCB} = 3.3V \pm 0.3V$		$V_{CCB} = 3.3V \pm 0.3V$			V		
	From	То		= 1.8V 15V		= 2.5V .2V	V _{CCA} = ±0.		V _{CCA}	= 2.5V .2V	
Parameter	(Input)	(Output)	Min.	Max.	Min.	Max.	Min.	Max.	Min.	Max.	Units
4	A	В	1.5	4.6	1.5	4.2	1.5	3.5	1.0	3.0	
t_{PD}	В	A	1.5	4.4	1.5	4.0	1.5	3.5	1.0	2.8	
$t_{\rm EN}$	ŌĒ	В	1.7	5.5	1.7	4.5	1.7	4.5	1.2	3.5	
$t_{ m DIS}$	ŌĒ	В	1.5	5.5	1.5	4.5	1.5	4.5	1.0	3.5	ns
$t_{\rm EN}$	ŌĒ	A	1.7	5.5	1.7	4.5	1.7	4.5	1.2	3.5	113
$t_{ m DIS}$	ŌĒ	A	2.0	5.5	2.0	4.5	2.0	4.5	1.5	3.5	
$t_{sk(o)}^{(1)}$						0.5				0.3	
$t_{sk(b)}^{(1)}$						0.3				0.25	

Note:

Operating Characteristics, $T_A = 25^{\circ}C$

Parameter	rs	Test Conditions	$V_{\rm CCA} = 2.5 V$ $V_{\rm CCB} = 3.3 V$ Typical	Units	
C. Down Dissinction Constitutes	Outputs Enabled (A or B)	$C_L = 30 pF,$	56	ωŪ	
C _{PD} Power Dissipation Capacitance	Outputs Disabled	F = 10 MHz	6	pF	

^{1.} This is the skew between any two outputs of the same package, and switching in the same direction. For $tsk_{(0)}$, Output 1 and Output 2 are any two outputs. For $tsk_{(b)}$, Output 1 and Output 2 are in the same bank.



Parameter Measurement Information, $V_{CCA} = 1.5V \pm 0.10V$

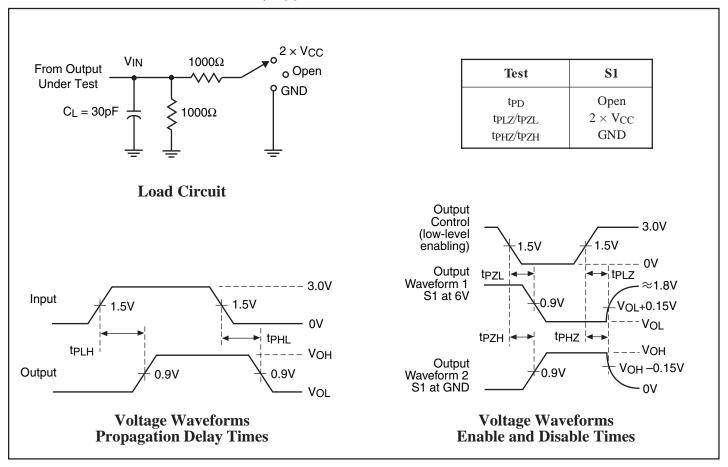


Figure 1. Load Circuit and Voltage Waveforms

Notes:

C_L includes probe and jig capacitance.

Waveform 1 is for an output with internal conditions such that the output is low except when disabled by the output control. Waveform 2 is for an output with internal conditions such that the output is high except when disabled by the output control. All input pulses are supplied by generators having the following characteristics: $PRR \le 10Mz$, $Z_0 = 50\Omega$, $t_R \le 2.5ns$, $t_F \le 2.5ns$.

The outputs are measured one at a time with one transition per measurement.

tPLZ and tPHZ are the same as tDIS.

t_{PZL} and t_{PZH} are the same as t_{EN}.

 t_{PLH} and t_{PHL} are the same as t_{PD} .



Parameter Measurement Information, $V_{CCA} = 2.5V \pm 0.2V$

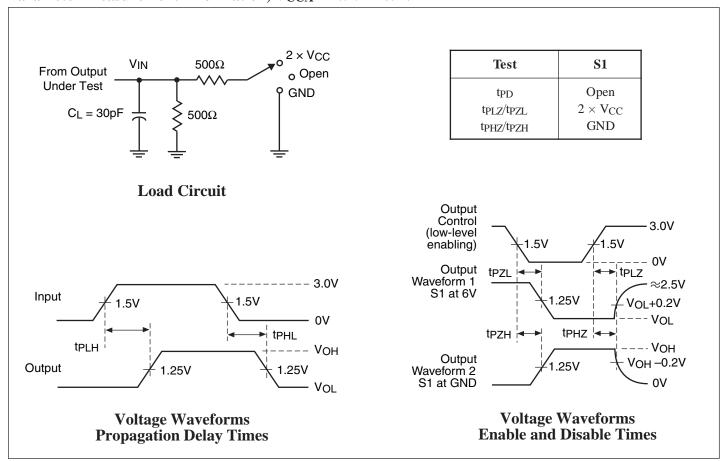


Figure 2. Load Circuit and Voltage Waveforms

Notes:

CL includes probe and jig capacitance.

Waveform 1 is for an output with internal conditions such that the output is low except when disabled by the output control. Waveform 2 is for an output with internal conditions such that the output is high except when disabled by the output control. All input pulses are supplied by generators having the following characteristics: $PRR \le 10Mz$, $Z_O = 50\Omega$, $t_R \le 2.5ns$, $t_F \le 2.5ns$. The outputs are measured one at a time with one transition per measurement.

tPLZ and tPHZ are the same as tDIS.

t_{PZL} and t_{PZH} are the same as t_{EN}.

tpLH and tpHL are the same as tpD.



Parameter Measurement Information, $V_{CCB} = 3.3V \pm 0.3V$

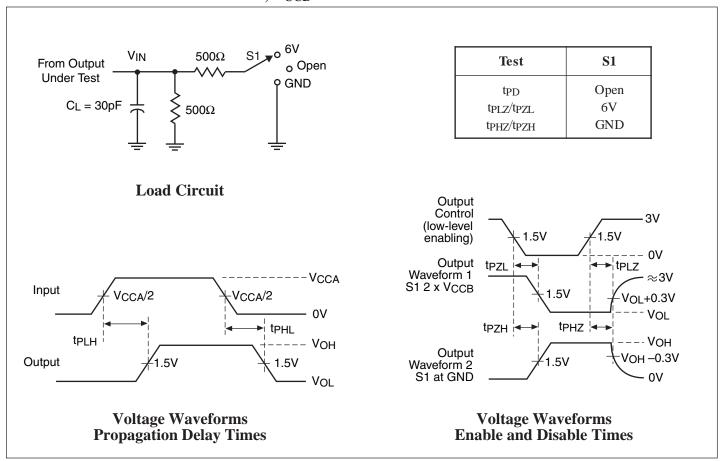


Figure 3. Load Circuit and Voltage Waveforms

Notes:

C_L includes probe and jig capacitance.

Waveform 1 is for an output with internal conditions such that the output is low except when disabled by the output control. Waveform 2 is for an output with internal conditions such that the output is high except when disabled by the output control. All input pulses are supplied by generators having the following characteristics: $PRR \le 10Mz$, $Z_O = 50\Omega$, $t_R \le 2.5ns$, $t_F \le 2.5ns$. The outputs are measured one at a time with one transition per measurement.

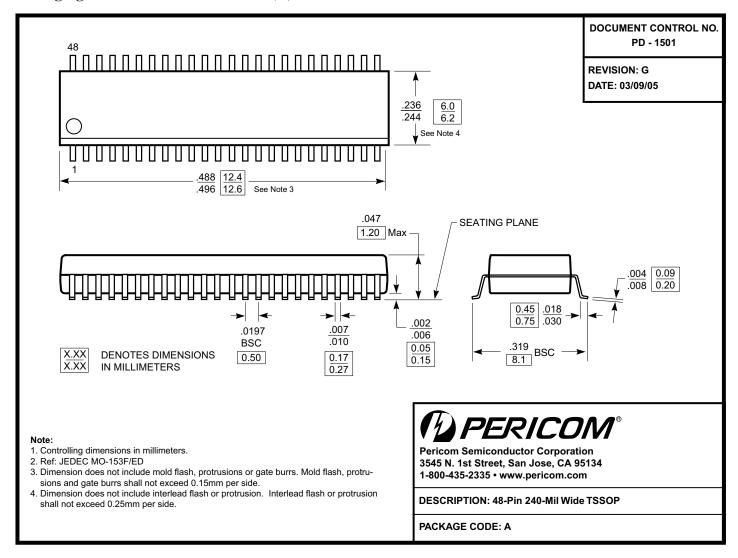
 t_{PLZ} and t_{PHZ} are the same as t_{DIS} .

t_{PZL} and t_{PZH} are the same as t_{EN}.

tpLH and tpHL are the same as tpD.

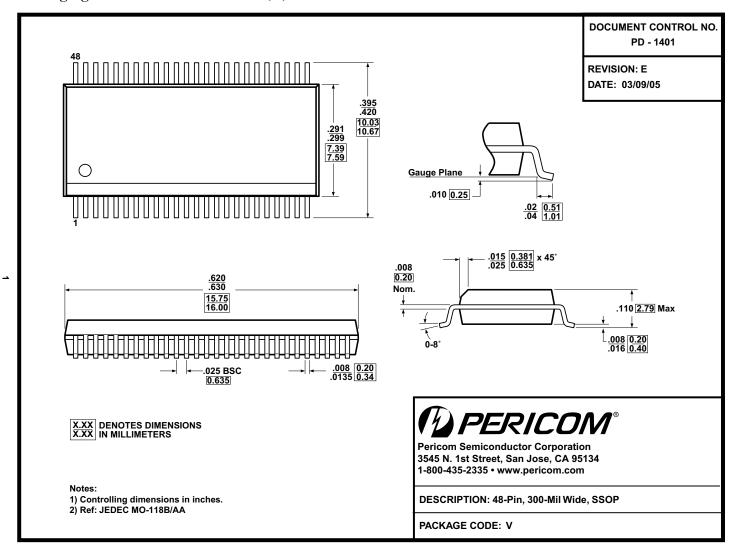


Packaging Mechanical: 48-Pin TSSOP (A)



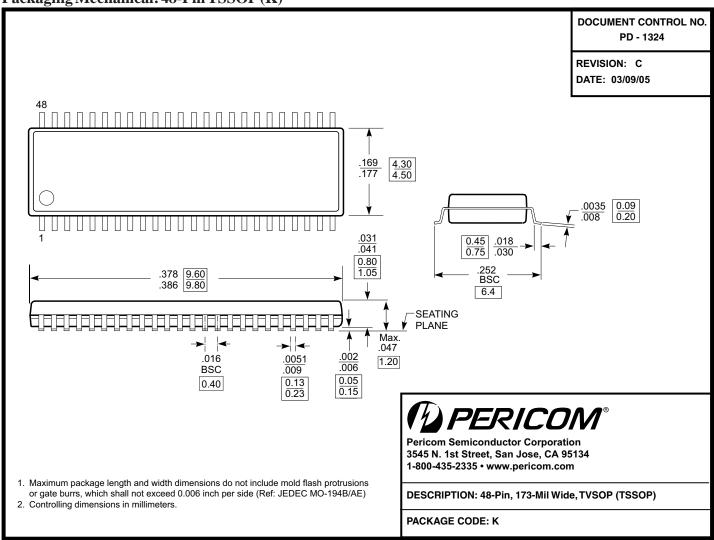


Packaging Mechanical: 48-Pin SSOP (V)









Ordering Information

Ordering Code	Package Code	Speed Grade	Package Description
PI74AVC164245VE	V	Blank	Pb-free & Green, 48-pin, 300-mil wide plastic SSOP
PI74AVC164245AE	A	Blank	Pb-free & Green, 48-pin, 240-mil wide plastic TSSOP
PI74AVC164245KE	K	Blank	Pb-free & Green, 48-pin, 173-mil wide plastic TVSOP
PI74AVC164245AVE	V	A	Pb-free & Green, 48-pin, 300-mil wide plastic SSOP
PI74AVC164245AAE	A	A	Pb-free & Green, 48-pin, 240-mil wide plastic TSSOP

Notes:

- 1. Thermal characteristics can be found on the company web site at www.pericom.com/packaging/
- 2. E = Pb-free and Green
- 3. X Suffix = Tape/Reel